



Estd: 2001

Sri Venkateshwara College of Engineering
Vidya Nagar, Bengaluru 562157

Format No.	ACD49A
Rev. No.	01
Date	01/08/2016
Page	1 of 2

Minutes of Meeting

Programme: Electrical and Electronics Engineering

Meeting No.

Date: 15/03/2019

S.No.	Faculty Name	Signature
1	Dr. Vijayashree Budyal	
2	Mrs. Kursheed B	
3	Mrs. Anitha M	
4	Mrs. Prema V	
5	Mr. Sarvesh Araballi	
6	Mrs. Ramya p	
7	Mr. Pruthviraj B G	
8	Mr. Pradeep M	
9	Mr. Basavaraja R	
10	Mr. Murgesh P D	
11	Mrs. Harshithananda B	
12	Mr. Nandaraja S H	
13	Mrs. Deepa K R	

Faculty Members Present

Non-Teaching Faculties Present

Sl.No	Name	Signature
1	Mr. R V Ramesh	
2	Mr. Umashankar D	
3	Ms. Gayithri G	
4	Mr. Ravi N R	

Agenda:

1. Planning to organize certification course for final year students.
2. Finalizing the Topic and Date for conducting certification Course.

Signature of HOD



Programme: Electrical and Electronics Engineering

Date: 15 /03/2019

Action Plan for this meeting

1. 5 Days workshop on PLC and SCADA From 1st April to 5th April for final year students.
2. Discussed about selection of Company for certification Course and Syllabus to cover.
3. Decision Taken for collecting fees from Students.
4. Schedule is planned for smooth run of workshop.
5. Assigned Roles and responsibilities to faculty for smooth conduction.


Signature of HOD

PROGRAMMABLE LOGIC CONTROLLER

Theory : 28 Hrs

Practical : 10 Hrs

UNIT 1

PLC basics: plc system, internal architecture I/O modules and interfacing, CPU processor, programming equipment, programming equipment, [programming formats construction of PLC ladder diagrams, devices connected to I/O modules. [6hrs]

UNIT 2

PLC programming input output devices: Mechanical switches, proximity switches, Photoelectric sensors and switches: Temperature sensors, position / displacement sensors; Strain gauge sensors; Pressure sensors: Liquid level detectors: Fluid flow measurement; smart sensors: output devices: Relay: directional control valves; Motors; Stepper motors; operational procedures, programming examples and PLC applications. [8hrs]

UNIT 3

Digital logic gates programming in the Boolean algebra system, conversion examples Ladder diagrams for process control; Ladder diagrams & sequences listings, ladder diagram construction and flowchart for spray process system. [6hrs]

Unit 4

PLC registers: Characteristics of registers, module addressing, holding registers, Input Registers, Output registers.

PLC functions: Timer functions and industrial applications, counters, counter function industrial applications. Arithmetic functions Number comparison functions number conversion functions.

Analog PLC operation: Analog modules & systems, Analog signal processing, Multi bit Data Processing Analog output Application Examples, PID principles, position indicator with PID control, PID modules PID tuning, PID function. [8hrs]

PLC certification course syllabus

To
Prof SAR
BL